

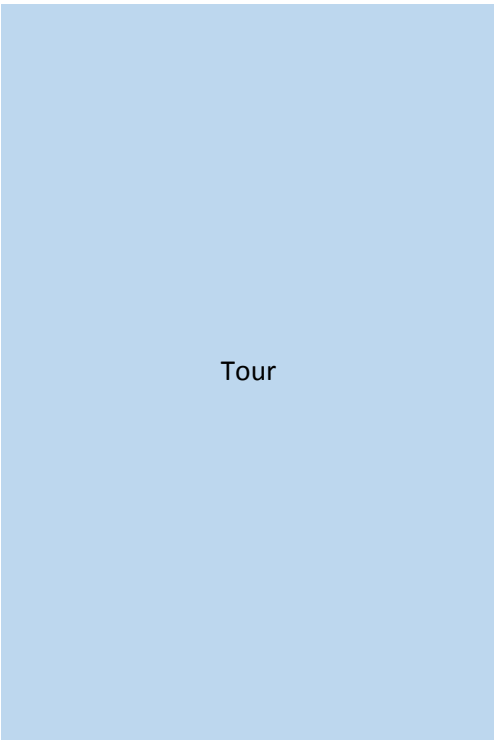
Smart SysTech 2018 - Preliminary Program

Time	Day #1	Time	Day #2
8:00 AM		8:00 AM	
8:05 AM		8:05 AM	
8:10 AM		8:10 AM	
8:15 AM		8:15 AM	
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11:30 AM		11:30 AM	
	Registration		Registration
			HF RFID Card Optimization Regarding the IC's Non-Linearity
			Read Rate Optimization of an RFID Tunnel Gate by Using the Concept of an Electromagnetic Reverberation Chamber With Different Q Factors
			Realization and Measurements of a Novel Dual Frequency Circulator for POW-RFID
	Opening Session		Coffee Break
			Analog Frontend for Ultra Low Power 60-GHz RFID Tag for Back-Scattering Communication
	Keynote #1		A comparison of several ASK modulator techniques for SHF Reader implementation

11:35 AM	Lunch Break
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12:30 PM	SHM of Concrete Bridge Structures using Wireless Sensor Networks
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12:55 PM	Wireless Sensors for Industry 4.0 – Wireless Communication and Wireless Powering
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1:20 PM	Smart Grid Data Classification with Deep Networks
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1:45 PM	LoRaWAN for IoT Applications in Air Cargo - Development of a GSE Tracking System for DHL Air Cargo Hub Leipzig
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2:10 PM	Coffee Break
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11:35 AM	A Channel Sounding System for Highly Interfered License-Exempt Frequency Bands
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12:00 PM	Lunch Break
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1:00 PM	Machine Learning Based Indoor Localisation Using Environmental Data in PhyNetLab Warehouse
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1:45 PM	A GUI-based Platform for Quickly Prototyping Server-side IoT Applications
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2:10 PM	Blockchain Cloud Manufacturing: Shop Floor and Machine Level
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Social Event

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